Δ

APPI	LICAE	BLE S	STANI	DARD										
OPERATING TEMPERATUR			ATING		-35°C TO +85°C (NO	TES 1)		RAGE PERATI	IRE RANG	ie	-10°C TO +60	°C		
RAT	ING	VOLT		RANGE	30V AC	IES I)	APPI	LICABLE		E RANGE	DF40C-*DP-0. 4V (
		CURRENT			0. 3A		3014		-	\top		. ,		
		CUKK	ENI		SPEC	IFIC/		NIS						
	ITI	EM			TEST METHOD	11 107	110			EOU	IREMENTS	QT	АТ	
CON			ION		1E21 METHOD				K	EQU	IKEWEN 15	QI	AI	
CONSTRUCTION GENERAL EXAMINATION				VISUALLY AND BY MEASURING INSTRUMENT.				ACCO	RDING T	O DR	RAWING.	Х	Х	
MARKING				CONFIRMED VISUALLY.								X	X	
ELECTRIC CHARA								•						
CONT	ACT R	ESIST	ANCE	20mV AC	OR LESS 1khz,1m A .			$90 m\Omega$	MAX.			Х	_	
INSULATION RESISTANCE				100V DC.				50MΩ MIN.				Х	-	
VOLTAGE PROOF				100V AC FOR 1 min.				NO FL	NO FLASHOVER OR BREAKDOWN.				_	
MEC	HAN	ICAL	. CHA	RACTE	RISTICS			1						
MECHANICAL OPERATION				30TIMES INSERTIONS AND EXTRACTIONS.				 CONTACT RESISTANCE: 90mΩ MAX. NO DAMAGE, CRACK OR LOOSENESS OF PARTS. 				X	_	
VIBRATION				SINGLE AMPLITUDE 0.75 mm,10CYCLES, FOR 3 DIRECTIONS.				 NO ELECTRICAL DISCONTINUITY OF 1 μs. NO DAMAGE, CRACK OR LOOSENESS OF PARTS. 				X	-	
SHOCK				490 m/s ² DURATION OF PULSE 11 ms AT 3 TIMES FOR 3 DIRECTIONS.				 NO ELECTRICAL DISCONTINUITY OF 1 μs. NO DAMAGE, CRACK OR LOOSENESS OF PARTS. 				X	_	
ENV	IRON	IMEN	JTAL	CHARA	CTERISTICS			1					I	
RAPID CHANGE OF TEMPERATURE				TIME $30\rightarrow 5 \text{ MAX} \rightarrow 30 \rightarrow 5 \text{ MAX min}$				 CONTACT RESISTANCE: 90mΩ MAX. INSULATION RESISTANCE: 50MΩ MIN. NO DAMAGE, CRACK OR LOOSENESS OF PARTS. 					_	
DAMP HEAT (STEADY STATE)								CONTACT RESISTANCE: 90mΩ MAX. INSULATION RESISTANCE: 25MΩ MIN. NO DAMAGE, CRACK OR LOOSENESS OF PARTS.				· ·		
SULPHUR DIIOXIDE								① CONTACT RESISTANCE: 180mΩ MAX. ② NO DAMAGE, CRACK OR LOOSENESS X OF PARTS.					-	
HEAT RESISTANCE OF SOLDERING				SOLDERING AREA				NO DEFORMATION OF CASE OF EXCESSIVE LOOSENESS OF THE TERMINASL.				Х	_	
SOLDERABILITY				SOLDERING TEMPERATURE: 245±5°C DURATION OF IMMERSION: SOLDERING FOR 3				A NEW UNIFORM COATING OF SOLDER SHALL COVER MINIMUM OF 95% OF THE SURFACE BEING IMMERSED.						
. (COUNT	Γ	DE	SCRIPTIC	N OF REVISIONS		DESIG	SNED	NED		CHECKED	DA	DATE	
2 DEMARKS				DIS-H-00001152 SH. H03			SODA	TS. MIYAZAKI		+	12. 03			
REMARKS NOTE1: INCLUDE THE TEMPERATURE RISING BY CURRENT								CHECKED TS. MIYAZAKI 11. 0					08. 03	
Unless otherwise specified, refe					er to JIS C 5402.				DESIGN		YH. MICHIDA YH. MICHIDA		08. 03	
			•		AT:Assurance Test X:Applicable Test					ELC-311352-	11.08.03			
K	ב	SPECIFI HIROSE E			CATION SHEET LECTRIC CO., LTD.		PART	NO.		DF40C-*DS-0. 4V (51)				
П	7						CODE	NO.	. CL684		Λ	1/1		

FORM HD0011-2-1