



DFLS130

1.0A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER POWERDI123

Features

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Surge Capability
- High Current Capability and Low Forward Voltage Drop
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: PowerDI123
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.01 grams (Approximate)



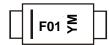
Ordering Information (Note 4)

| Part Number | Case | Packaging |
|-------------|------------|------------------|
| DFLS130-7 | PowerDI123 | 3000/Tape & Reel |

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



F01 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: D = 2016)M = Month (ex: 9 = September)

Date Code Key

| rear | 2013 | | 2014 | 2015 | | 2016 | 2017 | | 2018 | 2013 | 9 | 2020 |
|-------|------|-----|------|------|-----|------|------|-----|------|------|-----|------|
| Code | Α | | В | С | | D | Е | | F | G | | Н |
| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 | N | D |

1 of 4



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

| Characteristic | Symbol | Value | Unit |
|---|--|-------|------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | V _{RRM} V _{RWM} V _R | 30 | ٧ |
| RMS Reverse Voltage | V _{R(RMS)} | 21 | V |
| Average Forward Current | I _{F(AV)} | 1.0 | A |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load | I _{FSM} | 35 | А |

Thermal Characteristics

| Characteristic | Symbol | Тур | Max | Unit |
|---|----------------|-----|------|------|
| Power Dissipation (Note 5) | P _D | _ | 1.67 | W |
| Power Dissipation (Note 6) | P _D | _ | 556 | mW |
| Thermal Resistance Junction to Ambient (Note 5) | $R_{	heta JA}$ | _ | 60 | °C/W |
| Thermal Resistance Junction to Ambient (Note 6) | $R_{	heta JA}$ | _ | 180 | °C/W |
| Thermal Resistance Junction to Soldering (Note 7) | $R_{	heta JS}$ | _ | 10 | °C/W |

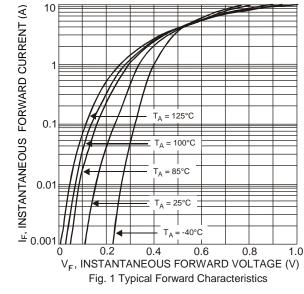
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

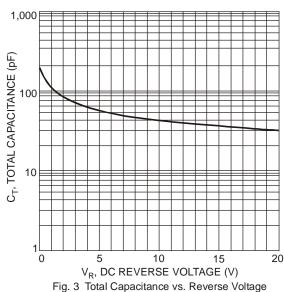
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition |
|------------------------------------|----------------|-----|------|------|------|---------------------------------|
| Reverse Breakdown Voltage (Note 8) | $V_{(BR)R}$ | 30 | | | V | $I_R = 1.5 \text{mA}$ |
| | | | 0.25 | | | $I_F = 0.1A$ |
| Forward Voltage (Note 8) | V_{F} | _ | 0.33 | 0.37 | V | $I_F = 0.7A$ |
| | | _ | 0.36 | 0.42 | | $I_F = 1.0A$ |
| Leakage Current (Note 8) | I_R | | 0.15 | 1.0 | mA | $V_R = 30V, T_A = +25^{\circ}C$ |
| Total Capacitance | C _T | | 40 | | pF | $V_R = 10V, f = 1.0MHz$ |

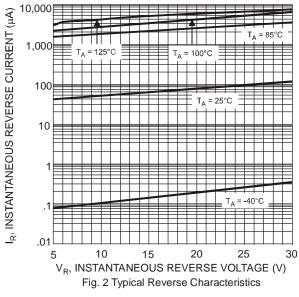
Notes:

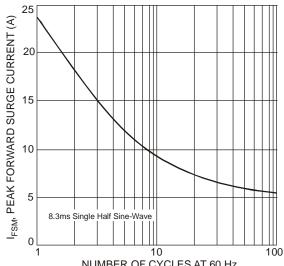
- 5. Part mounted on 50.8mm X 50.8mm GETEK board with 25.4mm X 25.4mm copper pad, 25% anode, 75% cathode.
- 6. Part mounted on FR-4 board with 1.8mm X 2.5mm cathode and 1.8mm X 1.2mm anode, 1 oz. copper pads.
- 7. Theoretical R_{0JS} calculated from the top center of the die straight down to the PCB cathode tab solder junction.
- 8. Short duration pulse test used to minimize self-heating effect.







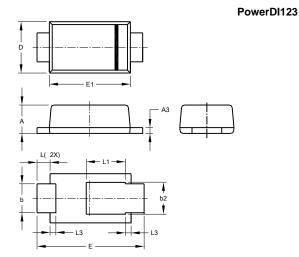




NUMBER OF CYCLES AT 60 Hz Fig. 4 Maximum Non-Repetitive Peak Forward Surge Current

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



| Dim | Min | Max | Тур |
|-----|-------|-------|------|
| Α | 0.93 | 1.00 | 0.98 |
| A3 | 0.15 | 0.25 | 0.20 |
| b | 0.85 | 1.25 | 1.00 |
| b2 | 1.025 | 1.125 | 1.10 |
| D | 1.63 | 1.93 | 1.78 |
| Е | 3.50 | 3.90 | 3.70 |
| E1 | 2.60 | 3.00 | 2.80 |
| ٦ | 0.40 | 0.50 | 0.45 |
| L1 | 1.25 | 1.40 | 1.35 |
| L3 | 0.125 | 0.275 | 0.20 |

All Dimensions in mm

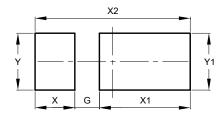
PowerDI123



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI123



| Dimensions | Value (in mm) |
|------------|------------------|
| G | 0.65 |
| Х | 1.05 |
| X1 | 2.40 |
| X2 | 4.10 |
| Y | 1.50 |
| Y1 | 1.50 |

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